

12/21/99
JCS715 U.S. PTO

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CIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

THE COMMISSIONER OF PATENTS
Washington, D.C.

Docket No.: 252103-4540

Sir: Transmitted herewith for filing is the patent application of

Inventor(s): Fu-Tai Liou and Wen-Kuan Yeh

For: **STRUCTURE FOR ESD PROTECTION WITH SINGLE CRYSTAL SILICON SIDED JUNCTION DIODE**

Enclosed are the following documents:

- ☒ 8 Pages of Specification
- ☒ 4 Pages of Claims
- ☒ 1 Pages of Abstract
- ☒ 3 Sheets of Drawings (Formal)
- ☒ An Assignment of the Invention
- ☒ Recordation Cover Sheet
- ☒ Combined Declaration and Power of Attorney

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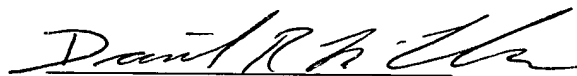
CLAIMS AS FILED BY A LARGE ENTITY						
	Total Claims Presented		Claims Allowed in Basic Fee	Present Extra	Rate	Basic Fee: \$ 760.00
Total Claims	20	Minus	20		x \$ 18.00	\$
Indep. Claim	3	Minus	3		x \$ 78.00	\$
[] First Presentation of multiple Dependent Claims					+ \$250.00	\$
[] Surcharge - Late Filing of Fee and/or Declaration					+ \$130.00	\$
[X] Assignment Recordation Fee					+ \$ 40.00	\$ 40.00
Total Filing Fee						\$ 800.00

☒ The Commissioner is Hereby Authorized to Charge any Additional Fees Which may Be Required, or Credit Any Overpayment to Deposit Account No. 20-0778. A duplicate Copy of this Sheet is Enclosed.

☒ A Check in the Amount of \$760.00 is attached.

☒ A Check in the Amount of \$40.00 is attached.

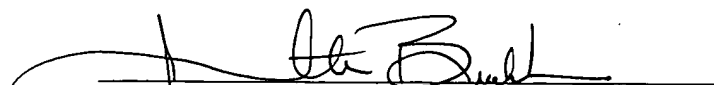
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EXPRESS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on December 21, 1999.

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Signature - Jeannette Buehler